

## THIAGARAJAR COLLEGE OF ENGINEERING - MADURAI 625 015 TCE-III

S.No	One credit course need analysis sheet		
1.	Name of the Course	Design of Power Supplies CDAC, Thiruvananthapuram	
2.	Name of the Industry	CDAC, Thiravananthapuram	
3.	Name of the SIG associated with		
4.	Motivation for offering the course		
4.1 Feedback (If yes, Details of the feedback as per the annexure I)		the annexure I)	
	From Recruiter	Y/N⁄	
	From Employer	Y/N⁄	
	From Alumni	Y/N/	
	From Academic Council members	Y/N_	
	From Board of Studies members	Y/N⁄	
	From Senior students	Y/N_	
	From current students	Y/N⁄	
	From Performance Assessment	Y/N	
:	Committee From Department Advisory	Y/N/	
	committee	P (If yes, details)	
4.2	Faculty participation in Seminar/FDP (If yes, details)  At higher learning institutes		
	At Industry		
5.	Outcomes expected		
	Technology transfer	·	
	Student Internship		
	Placement FDP/seminar at TCE		
	Organizing FDP/seminar at TCE		
	Collaborative research/consultancy		
	projects Tracing in the		
	Faculty as Trainee/Trainer in the		
	Industry		
	Joint publications		
-	Setting up of Lab/Infrastructure		



#### Course Schedule

Name of the Course:

Design of lower Supplies CDAC, Thiruvananthapuram Mr.E. Chandrasekar Name of the Industry:

Name of the Expert:

Number of Students enrolled:

Name of the Faculty:

Dr. B. Ashok Kumar

Date/Time/Venue:

List Enclosed

		Tonics	Remarks
Date	Time	Topics	
Day1			
:			
Day2			
Day2	· · ·		
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Signature of the Faculty coordinator

# Thiagarajar College of Engineering, Madurai-15 (A Govt. Aided ISO 9001:2008 Certified Autonomous Institution affiliated to Anna University)

Department of Electrical & Electronics Engineering

06.03.17

#### One credit course 14EE1KO-Design of Power Supplies

#### Schedule

Date	9.30 AM -11.00 AM	11.15 AM-1.15 PM	2.00 PM - 3.30 PM	3.45 PM-4.45 PM
04.03.17	Introduction of Available Sources & demanding loads: Sources - AC mains, Lab supplies, Batteries, Solar Cells Loads - Requirements of load, battery as load,	Selection of Topology: Step-Up / Step-Down, Multiple outputs, Continuous & discontinuous modes of operation, Isolated converters, Various configurations of Converters,	Selection of Components: Selection of Resistors, Chokes, Capacitors, Diodes, MoSFETs & IGBTs, Connectors Guide to Instrumentation: Basics of measurements using DMM, Oscilloscope, Electronic loads, etc Design of Magnetics Fundamentals & ideal conditions, design of High frequency chokes & transformers, Selection of wire gauge, sealing of magnetics	Design of Feedback circuits Basic control requirements, Current & voltage mode control fundamentals & system stability conditions Design of Control and Monitoring circuits Practical Control circuitry & Monitoring circuitry requirements
05.03.17	9.00 AM - 11.00 AM  Evaluations and Thermal management Performance evaluations of SMPS & thermal loss calculations and cooling options & packaging of converter EMI control requirements Overview of EMC, differentiating signal and noise, Layout concepts Low & High frequency filtering requirements, Optimal filter design	Worst case analysis Introduction to datasheet reading, operation tuned to datasheet, typical worst case analysis Simulation of particular application in PSIM Simulation of simple BUCK, BOOST & BUCK BOOST, Typical discrete power factor corrector circuit	2.00 PM - 3.30 PM  Standards governing the power supplies IEC standards for Electrical & Environmental testing, certification standards, Ingress protection standards Recent trend in Power supplies Recent advancements in components, Recent advancements in topologies,	3.45 PM-4.45 PM Digital control of power supplies, Power Integration & its Low power applications Demonstration of Various SMPS & Its components-Practical design of a cell phone charger- a practical approach



### THIAGARAJAR COLLEGE OF ENGINEERING - MADURAI 625 015

Course Instructor Feedback for One/Two credit course

TCE-III

Name of the Course:

Design of power supplies CDAC, Thiruvaranthapuram Mr. E. Chandrasekar

Name of the Industry:

Name of the Expert:

Date/Time/Venue:

	Comments
Student attendance	48
Level of the students in understanding the concepts	Grood
Any suggestions regarding new content to be included as Prerequisites/Special electives	Practical session can be conducted.
Hall/Lab arrangements	Good
Hospitality	Grood

8. IC

Signature of Head of the Department

## Thiagarajar College of Engineering, Madurai-15 (A Govt. Aided ISO 9001:2008 Certified Autonomous Institution affiliated to Anna University)

Department of Electrical & Electronics Engineering

04.03.17

### One credit course on 14EE1KO Design of Power Supplies

Students Attendance Semester: III EEE (A) 05.03.17 05.03.17 04.03.17 04.03.17 NAME OF THE STUDENT REG NO AN S.NO FN AN FN AMKK AMKSK Markisk AARTHI.S.K 14E01 Politicalet Politicalet 1. Polhabot P Multok ABHISHEK.P 14E03 2. ABINAYA.K K. Shiraya 14E04 3. AMALARANI.K 14E06 4. ARUNA DEVI.S 14E11 5. ARUN KUMAR.R R. Antima 14E12 6. AZHAGUMEENA.K o P 14E13 7. BALAJI.K.S 14E14 BALAJI.S 14E15 9. BALENDRA DAGEENDRAN 10, 14E17 BARAKATH AGEESHA.M.K 11 14E18 S. charles CHARLES.S 12 14E20 Ching CHINGKHEINGANBA.L 14E21 13 Dipila\_ People DEEPIKA.K 14, 14E23 P Div R.Z DIVYA.R 15 14E25 ELAKYA.A 16 14E27 ESWARAKALYANI.C 17, 14E28 Mass Go GAYATHRI.S.S SS Gayatt 18 14E29 GEETHANJALI.K 19 14E30 GOKUL NATH.G.R.P 20 14E31 GOKULA KRISHNAN.B 14E32 21 HARI HARAN.B 14E34 22 HARIDEVAN.G 14E35 23 JAISHRI.J 24 14E39 JAYALAKSHMI.G 14E41 25 N. Jugas JAYASREE.N 14E42 26 JENISH SWEETLIN Bewerty 14E43 27 BRIGHT.J fed Stelad JESLIN KEBA.J 14E44 28 ReynRobinh JEYA PRAKASH .C.K 14E45

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